

Benha University Faculty of Engineering Shoubra Electronic circuits (B)

Electrical Eng. Dept. 3<sup>rd</sup> year communication 2012-2013

## Sheet (8)

1. What are the basic components in a series regulator?

Control element, error detector, sampling element, reference voltage

**2.** A certain series regulator has an output voltage of 8V. If the op-amp's closed loop gain is 4, what is the value of the reference voltage?

2 V

**3.** How does the control element in a shunt regulator differ from that in a series regulator?

In a shunt regulator, the control element is in parallel with the load rather than in series.

**4.** What is one advantage of a shunt regulator over a series type? What is a disadvantage?

A shunt regulator has inherent current limiting. A disadvantage is that a shunt regulator is less efficient than a series regulator.

5. What are the three terminals of a fixed-voltage regulator?

Input, output, and ground

6. What is output voltage of a 7809? Of a 7915?

A 7809 has a +9 V output; A 7915 has a -15 V output.

7. What are the three terminals of an adjustable-voltage regulator?

Input, output, adjustment

**8.** What external components are required for basic LM317 configuration?

A two-resistor voltage divider

- **9.** (a) Determine the output voltage for the series regulator shown in figure 1.
  - (b) If R3 is increased to  $4.7K\Omega$ , what happens to output voltage?
  - (c) What is the output voltage if zener voltage becomes 2.7V?



$$V_{\text{OUT}} = \left(1 + \frac{R_2}{R_3}\right) V_{\text{REF}} = \left(1 + \frac{5.6 \text{ k}\Omega}{2.2 \text{ k}\Omega}\right) 2.4 \text{ V} = 8.51 \text{ V}$$

b.

For 
$$R_3 = 2.2 \text{ k}\Omega$$
:  
 $V_{\text{OUT}} = \left(1 + \frac{R_2}{R_3}\right) V_{\text{REF}} = \left(1 + \frac{5.6 \text{ k}\Omega}{2.2 \text{ k}\Omega}\right) 2.4 \text{ V} = 8.5 \text{ V}$   
For  $R_3 = 4.7 \text{ k}\Omega$ :  
 $V_{\text{OUT}} = \left(1 + \frac{R_2}{R_3}\right) V_{\text{REF}} = \left(1 + \frac{5.6 \text{ k}\Omega}{4.7 \text{ k}\Omega}\right) 2.4 \text{ V} = 5.23 \text{ V}$   
The output voltage **decreases by 3.27 V** when  $R_3$  is changed from 2.2 k $\Omega$  to 4.7 k $\Omega$ .  
**C.**

$$V_{\text{OUT}} = \left(1 + \frac{R_2}{R_3}\right) V_{\text{REF}} = \left(1 + \frac{5.6 \,\text{k}\Omega}{2.2 \,\text{k}\Omega}\right) 2.7 \,\text{V} = 9.57 \,\text{V}$$

- 10. (a) In shunt regulator of figure 2, Assume  $I_L$  remains constant and  $V_{IN}$  changes by 1V, what is the change in the collector current of Q1?
  - (b) If the maximum allowable input voltage is 25V, what is the maximum possible output current when the output is short-circuited? What power rating should R1 have?

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11. Determine the output voltage of IC voltage regulator seen in figure 3, if  $I_{ADJ}$ =50µA, then with no load connected, how much current is there through the regulator with neglecting the adjustment terminal current.



The regulator current equals the current through  $R_1 + R_2$ .

$$J_{\text{REG}} \cong \frac{V_{\text{OUT}}}{R_1 + R_2} = \frac{14.3 \text{ V}}{11 \text{ k}\Omega} = 1.3 \text{ mA}$$

a.

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12. Determine the minimum and maximum output voltages for the circuit in figure 4 if  $I_{ADJ}$ =50 $\mu$ A







Good Luck

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